FIG. 1

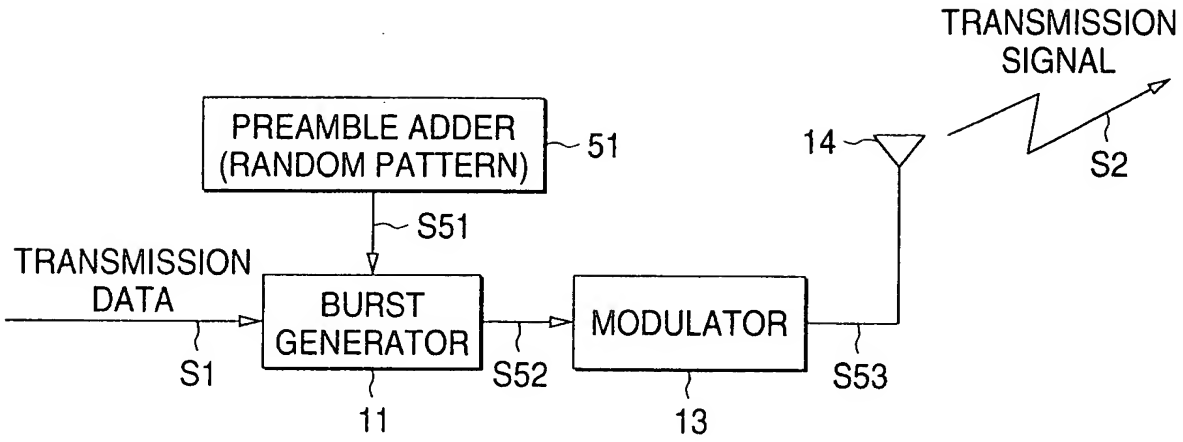


FIG. 2A

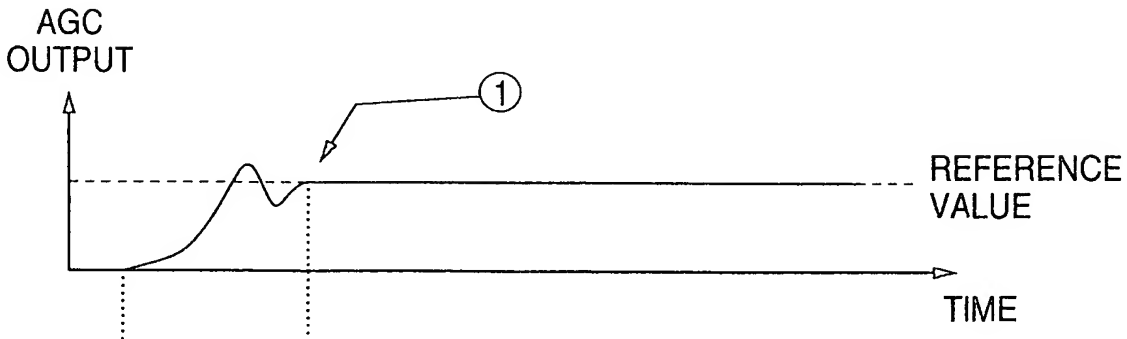


FIG. 2B

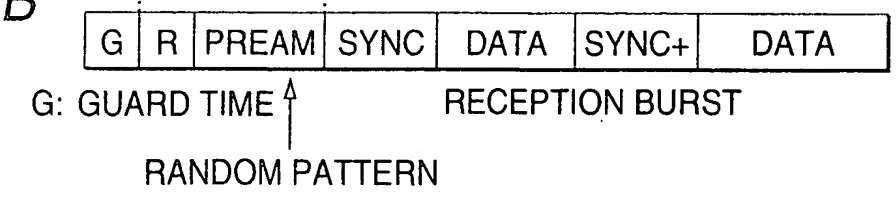


FIG. 3

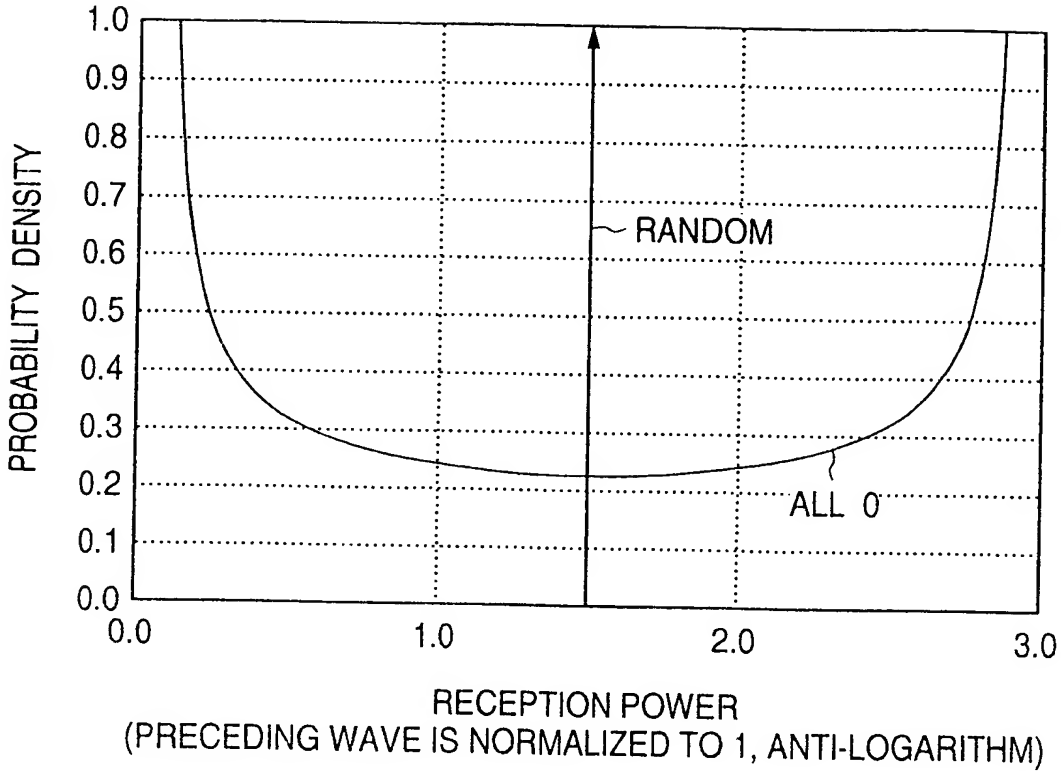

1.0
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0.0
PROBABILITY DENSITY
RANDOM
ALL 0
0.0
1.0
2.0
3.0
RECEPTION POWER
(PRECEDING WAVE IS NORMALIZED TO 1, ANTI-LOGARITHM)

FIG. 4

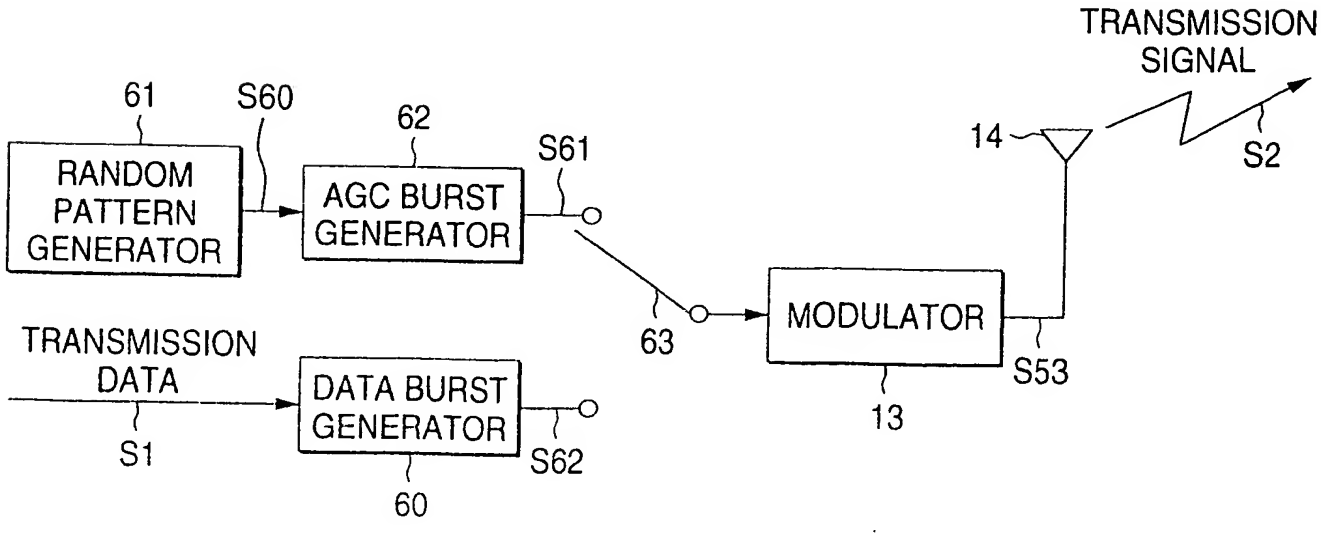

TRANSMISSION SIGNAL
61
S60
62
S61
RANDOM PATTERN GENERATOR
AGC BURST GENERATOR
14
S2
63
MODULATOR
S53
13
TRANSMISSION DATA
S1
DATA BURST GENERATOR
S62
60

FIG. 5

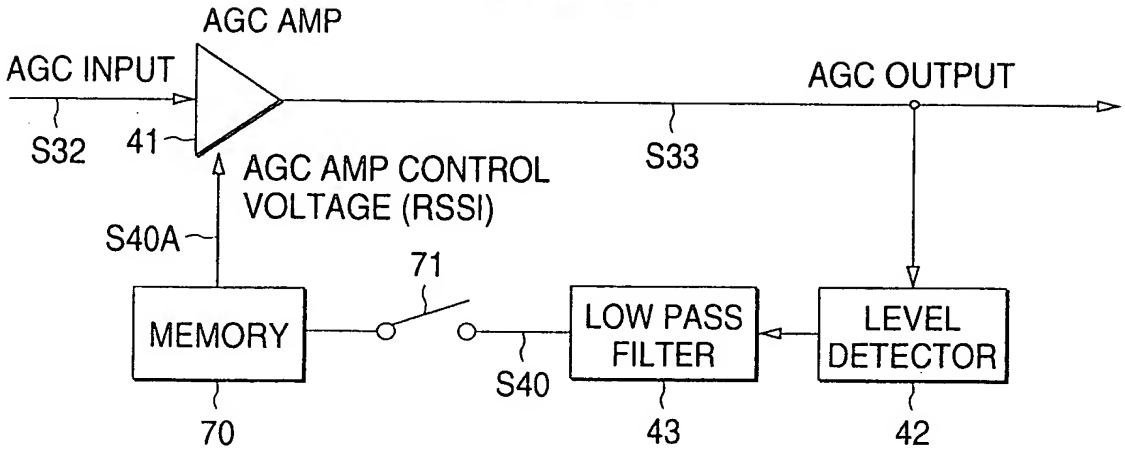

AGC AMP
AGC INPUT
AGC OUTPUT
S32
41
S33
AGC AMP CONTROL
VOLTAGE (RSSI)
S40A
71
MEMORY
LOW PASS
FILTER
LEVEL
DETECTOR
S40
70
43
42

FIG. 6

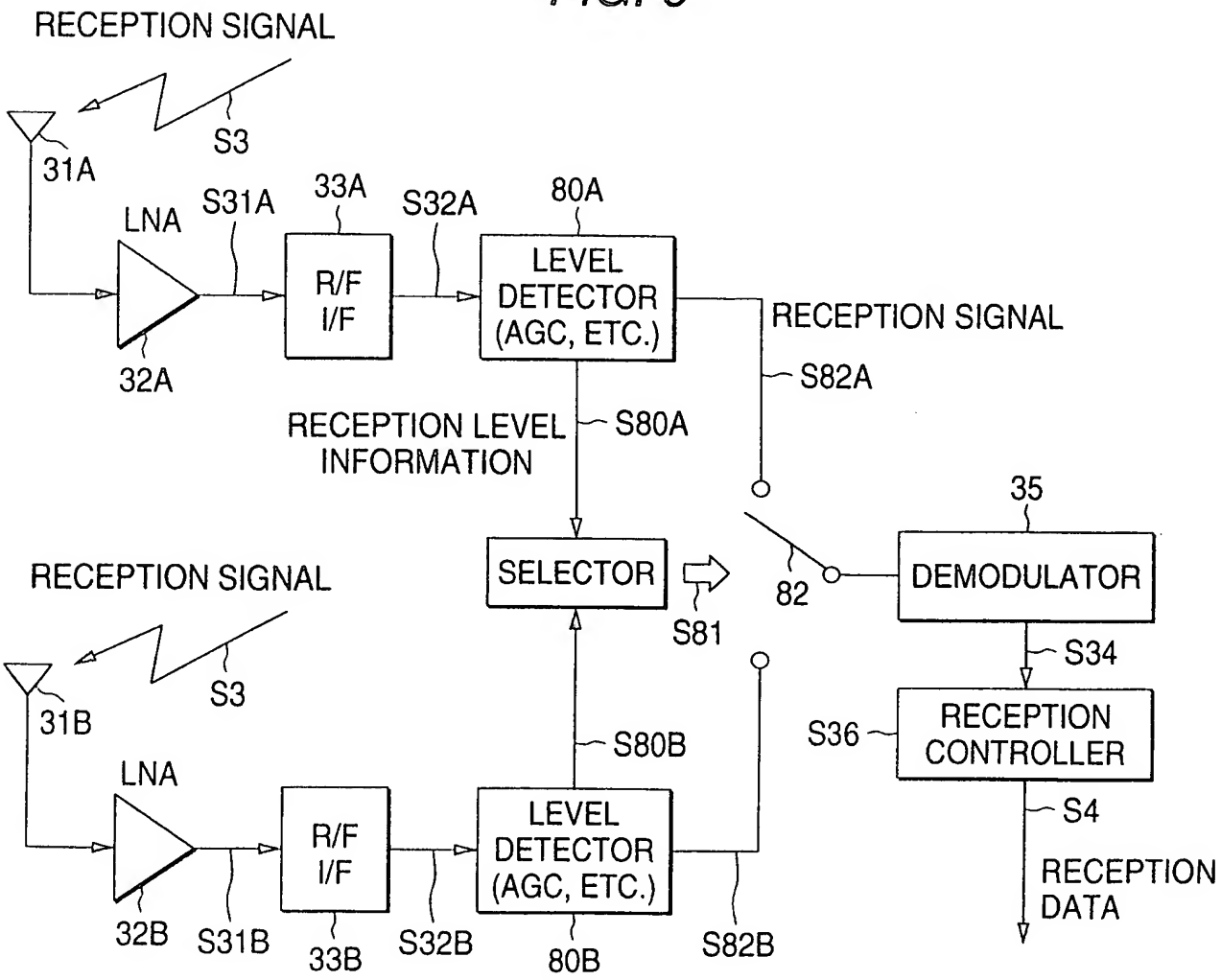

RECEPTION SIGNAL
S3
31A
LNA
S31A
33A
S32A
80A
R/F
I/F
LEVEL
DETECTOR
(AGC, ETC.)
RECEPTION SIGNAL
32A
S82A
RECEPTION LEVEL
INFORMATION
S80A
35
SELECTOR
S81
82
DEMODULATOR
RECEPTION SIGNAL
S3
31B
S34
S36
RECEPTION
CONTROLLER
S80B
LNA
S4
RECEPTION
DATA
R/F
I/F
LEVEL
DETECTOR
(AGC, ETC.)
32B
S31B
33B
S32B
80B
S82B

FIG. 7

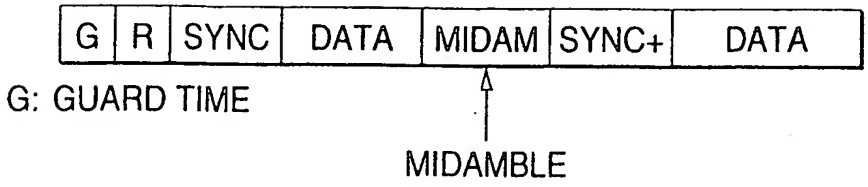


FIG. 8
PRIOR ART

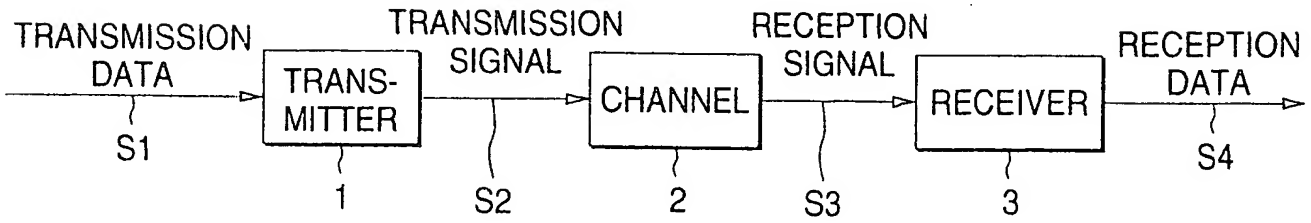


FIG. 9
PRIOR ART

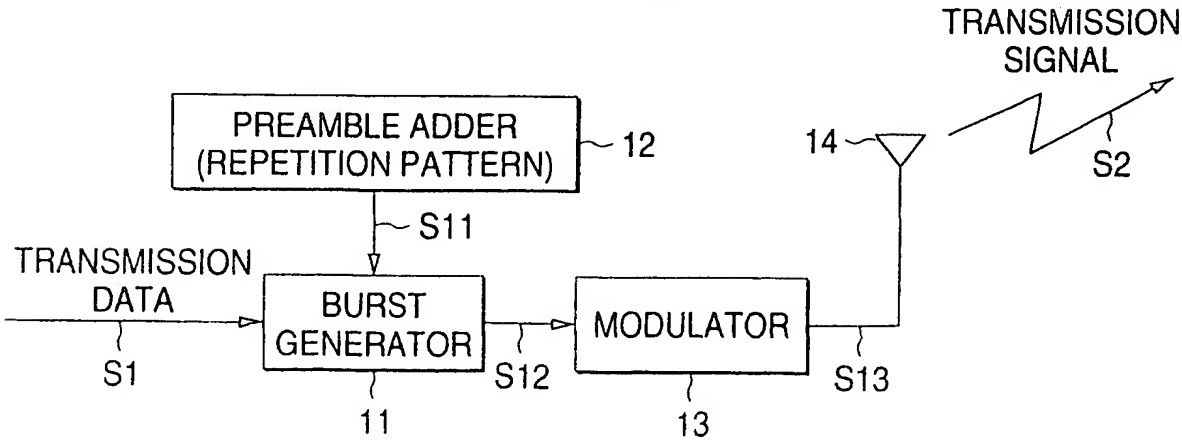


FIG. 10
PRIOR ART

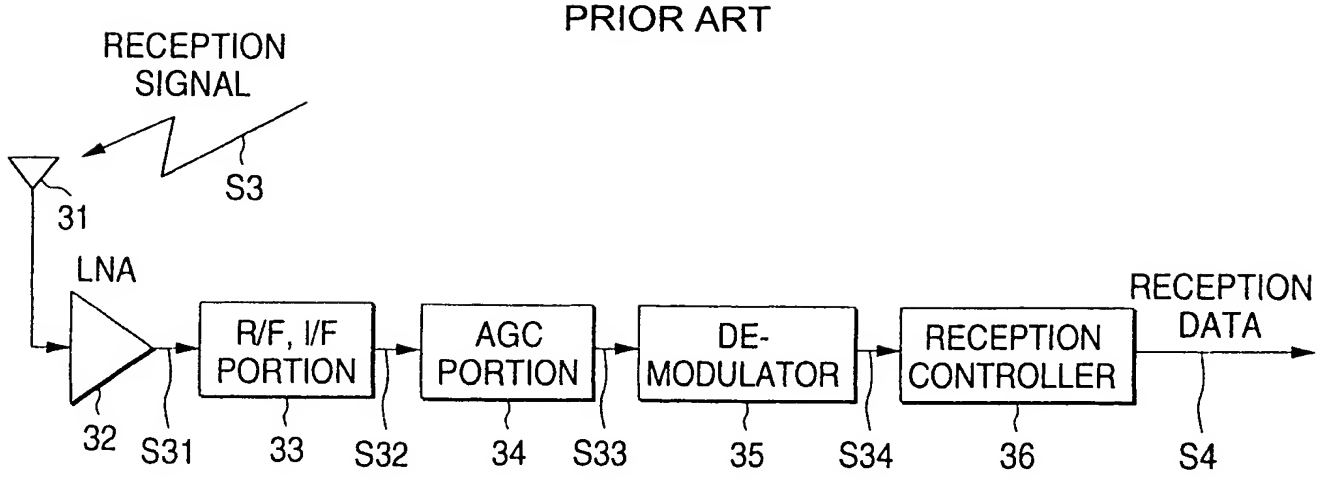


FIG. 11
PRIOR ART

G	R	PREAM	SYNC	DATA	SYNC+	DATA

G: GUARD TIME

FIG. 12
PRIOR ART

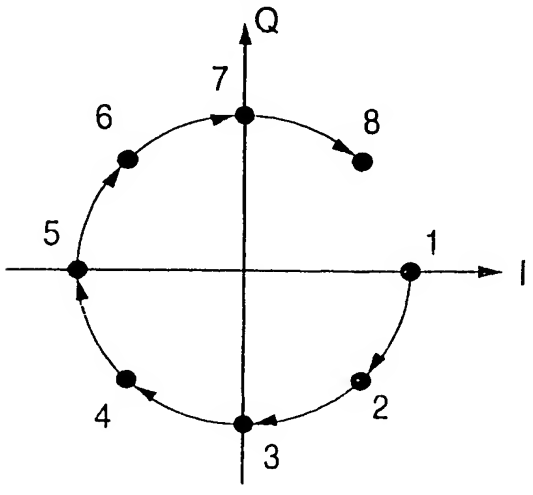


FIG. 13
PRIOR ART

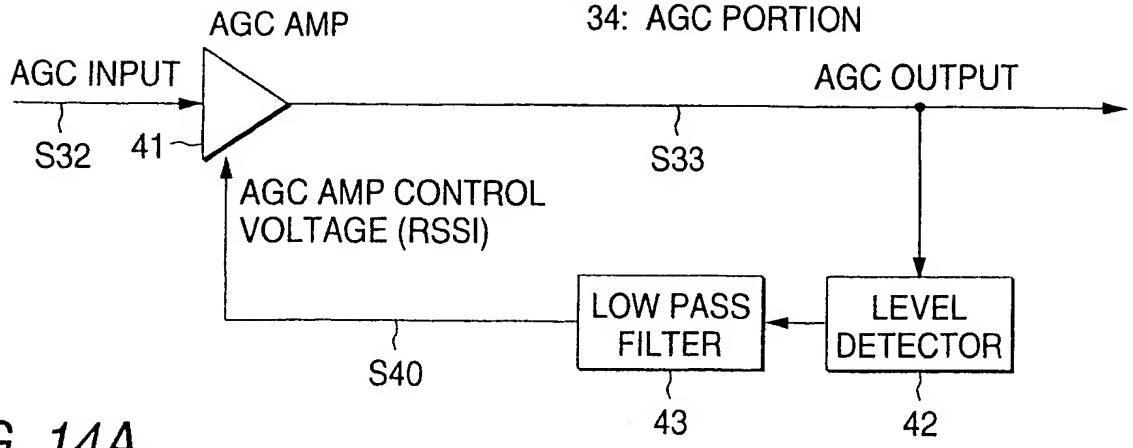


FIG. 14A
PRIOR ART

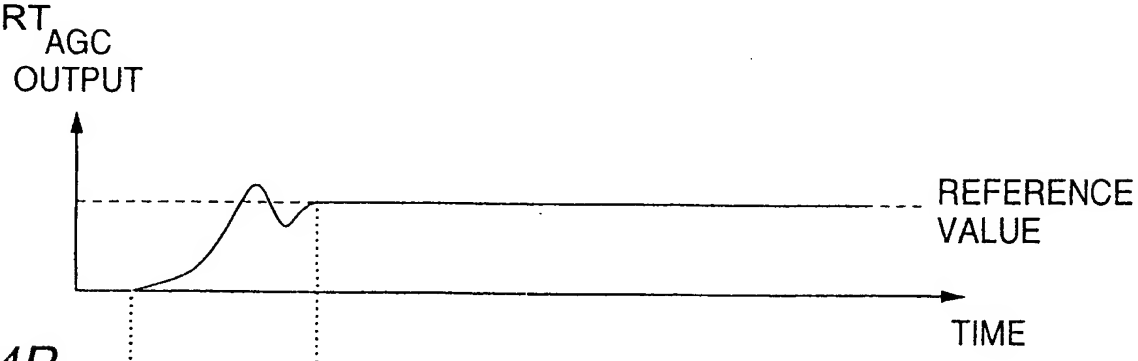


FIG. 14B
PRIOR ART

G: GUARD TIME RECEPTION BURST

FIG. 15

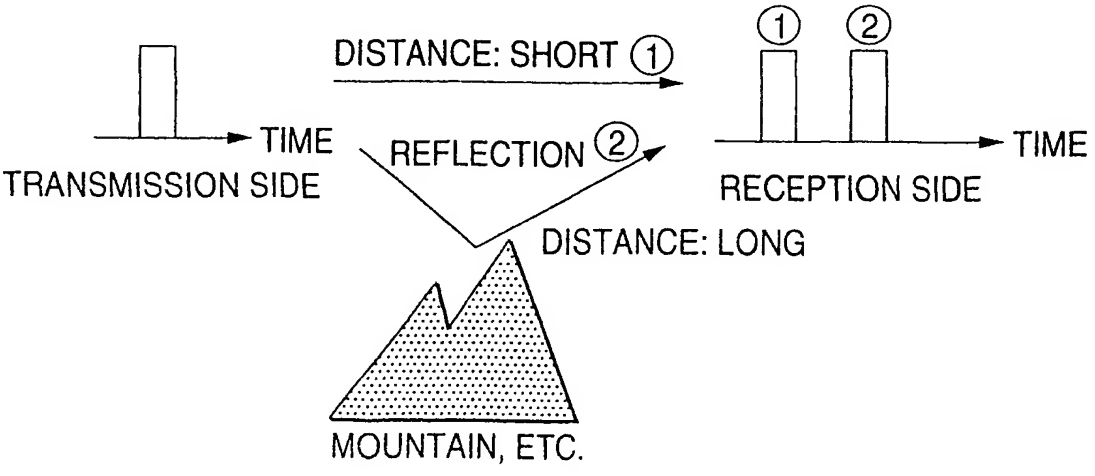


FIG. 16
PRIOR ART

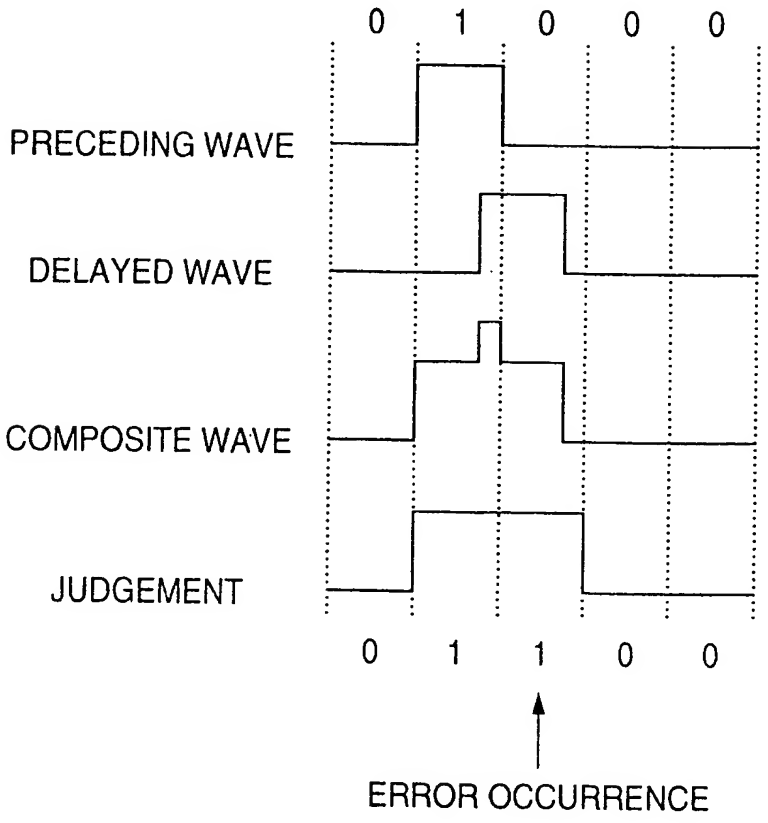


FIG. 17
PRIOR ART

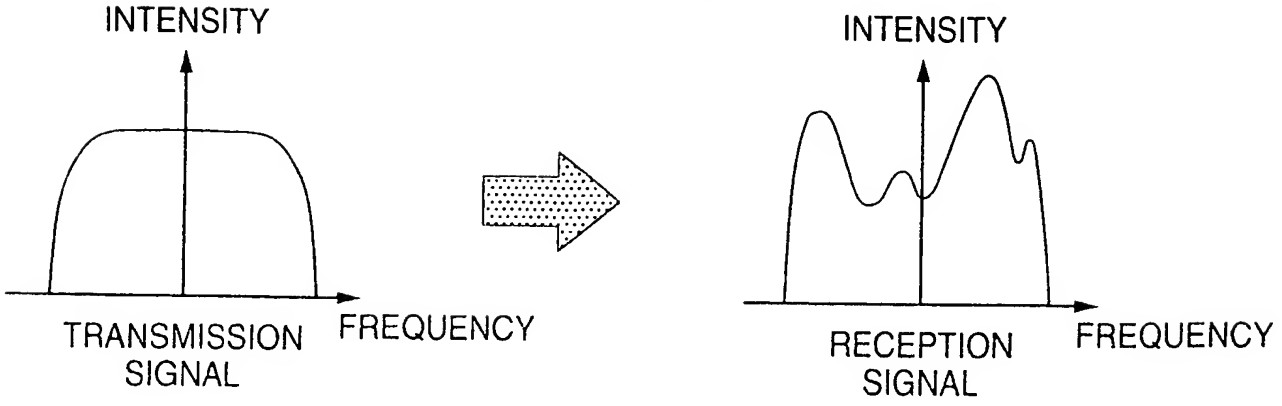


FIG. 18
PRIOR ART

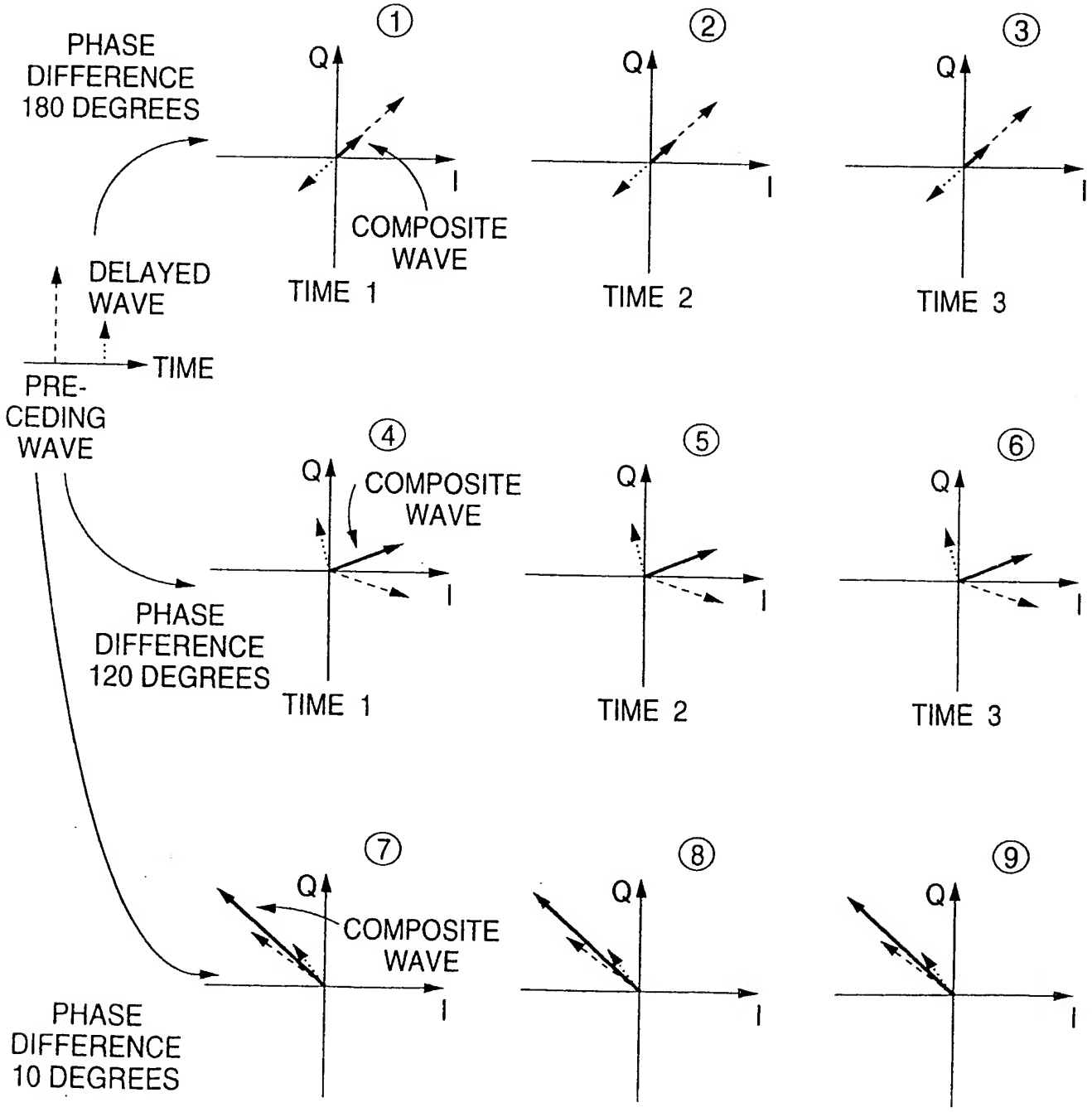


FIG. 19
PRIOR ART

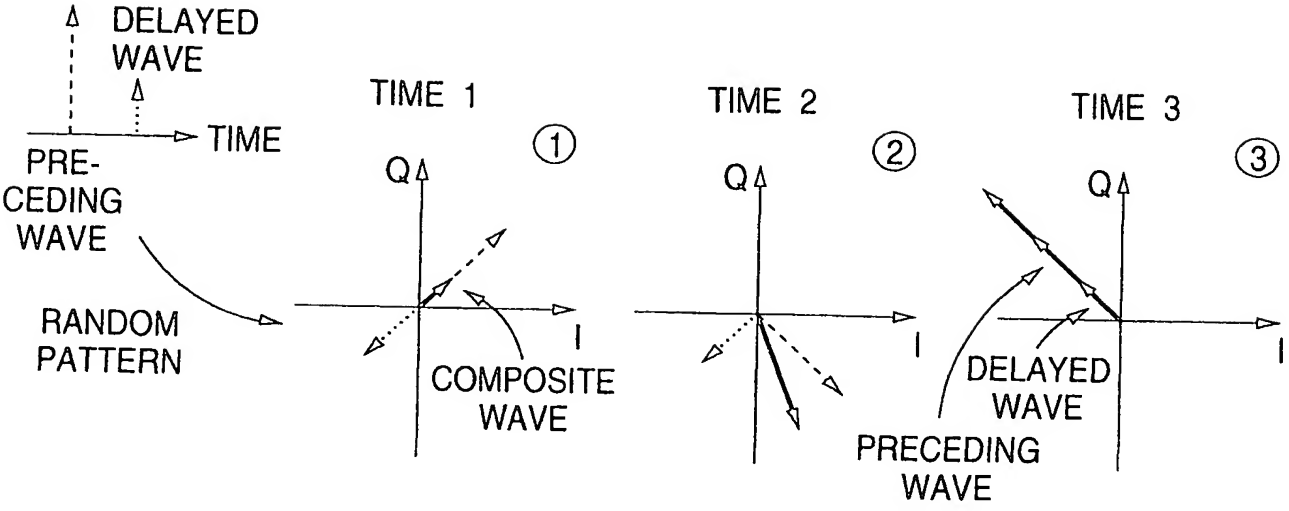


FIG. 20A
PRIOR ART

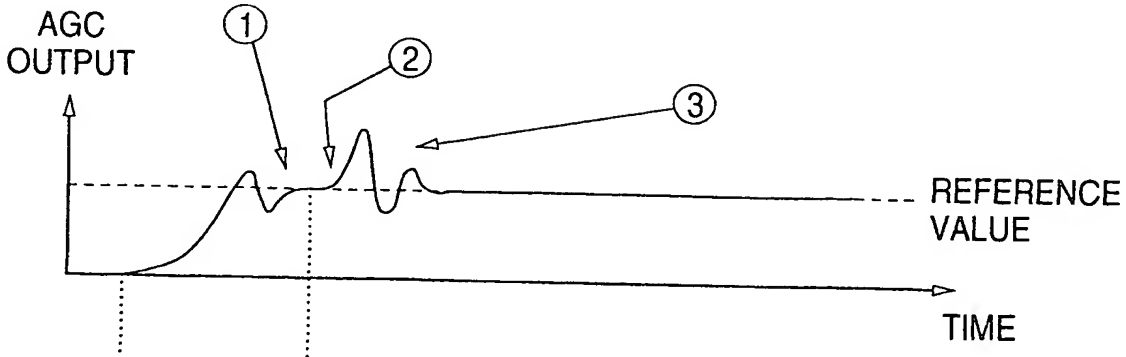
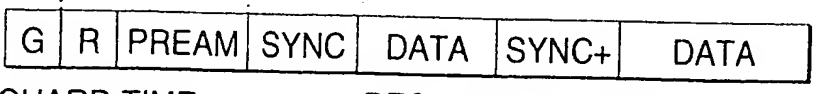


FIG. 20B
PRIOR ART



G: GUARD TIME RECEPTION BURST